UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,768,355 B1

DATED

: November 12, 2004

Page 1 of 1

INVENTOR(S) : Paul M. Henry et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item "[73] Assignee:

National Semiconductor Corporation,

Santa Clara, CA (US)" should read

-- [73] Assignee:

National Semiconductor Corporation,

Santa Clara, CA (US); and

Motorola, Inc.,

Schaumburg, IL (US) --.

Column 6,

Lines 63 and 64, "not inadvertently rest" should read -- not inadvertently reset --.

Signed and Sealed this

Twenty-third Day of August, 2005

JON W. DUDAS

Director of the United States Patent and Trademark Office